REMARKS

Careful review and examination of the subject application are noted and appreciated.

INTERVIEW SUMMARY

Examiner Day and Applicants' representative (Chris Maiorana) held telephone interviews on November 3, 2005, November 8, 2005 and November 9, 2005. The independent claims were discussed. An agreement was reached that would overcome the rejections of record. The amendments to the claims presented in this response reflect the agreement reached.

SUPPORT FOR THE CLAIM AMENDMENTS

Support for the claim amendments may be found in the specification, for example, on page 4 lines 10-11, page 11 lines 1-6, page 5 lines 8-11 and FIGS. 1-2, as originally filed. Thus, no new matter has been added.

CLAIM REJECTIONS UNDER 35 U.S.C. §112

The rejection of claims 1-11, 21-22 and 25-26 under 35 U.S.C. §112, first paragraph, has been obviated by appropriate amendment and should be withdrawn.

CLAIM REJECTIONS UNDER 35 U.S.C. §102

The rejection of claims 1-11 and 21-29 under 35 U.S.C. §102(b) as being anticipated by Tabatabaei et al. has been obviated by appropriate amendment and should be withdrawn. The claims discussed in the interview of November 3, 2005 have been presented with "Verilog model" replaced by "Hardware Description Language model" in independent claims 1 and 9. Claim 23 has been amended similar to claim 1. As such, the presently claimed invention is fully patentable over the cited references and the rejection should be withdrawn.

Regarding "model", the specification teaches in the first paragraph of the detailed description of the invention (page 4) that a system 100 provides verification of analog signals in models without reliance on the capabilities of any specific model language. The generic use of the term "model" continues in the specification and into the original claims. For example, see page 11, lines 1-6 and original claims 1, 7-9, 11-13 and 19. Per MPEP §2163 II.A.2, information which is well known in the art need not be described in detail in the specification. A reference to Hardware Description Language has been added as part of the agreement reached.

Accordingly, the present application is in condition for allowance. Early and favorable action by the Examiner is respectfully solicited.

The Examiner is respectfully invited to call the Applicants' representative should it be deemed beneficial to further advance prosecution of the application.

If any additional fees are due, please charge our office Account No. 50-0541.

Respectfully submitted,

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